SILVERSTONE

2U Single SERIES

2U 750Rx Gold / 2U 850Rx Gold

80 PLUS Gold 750W / 850W 2U Single Fixed cable power supply

Support standard 2U Single form factor
Supports 12V-2x6 PCIe connector and compatible ATX 3.1 and PCIe Gen 5 standard
Latest PCIe Gen 5 standard to support up to 2X power excursion
Black flat cable design
Main Japanese electrolytic capacitors
Silent running 60mm dual ball bearing fan
High efficiency with 80PLUS Gold certification
24/7 continuous power output with 50℃ operating temperature

SPECIFICATION

SilverStone 2U Single Series

2U 750Rx Gold / 2U 850Rx Gold

SST-2U750R-GF / SST-2U850R-GF

ATX Switching Power Supply
Cybenetics Gold efficiency certified.

750W / 850W

1.Scope

This document defines the Desktop Power Supply quality, 5 output 850W/750W, power supplies for
the application of Desktop systems.
The power supplies meet the buss structures of Intel platform, and the following key features:
(1)Input: Full Range (90-264Vrms) with Active Power Factor Correction.
(2)Output: Product is provided with a total of 5 output to meet the requirement of ATX12V platform.
(3)Cooling: A 60mm high reliable DC fan is used for cooling the power supply.

2.Electrical

The electrical specifications that follow is going to meet over the environmental ranges specified in Section 3 unless otherwise noted.

2.1 AC Input

Table 1 lists AC input voltage and frequency range for continuous operation. The power supply is capable of supplying full-rated output power over the input

Parameter	Min	Nominal Input	Max	Unit
V_{in} Voltage	90	100-240	264	Vrms
V_{in} Frequency	47	50/60	63	Hz
V_{in} Current / 850W		12A		A
Vin Current / 750W		10A		A
Power Factor(PF)		>0.95	at 230Vac input and full load	

Table 1. AC input

- The inrush current is less than 130A under the conditions of 230Vrms input and 25°C ambient cold start. The inrush current is limited to the extent that no damage will be done to the power supply under any specified line, load, and temperature conditions. The inrush current will not cause external protection devices (fuses) to trip.
- The leakage current of the power supply module is less than 3.5mA measured at 230Vac input.
- The repetitive ON/OFF cycling of AC input voltage will not damage the power supply.
- The primary fuse is installed for input over-current protection, and meet product safety requirement.

2.2 DC Output

2.2.1 DC Output Voltage Regulations

The DC output voltages remain within the regulation ranges shown in Table 2. when measured the at load end of the output connectors under all AC line, O/P loads, and environmental conditions. The voltage regulation will be maintained under continuous operation for a period of time equal to the MTBF specified in section 5.2 at any steady state temperature and operating conditions specified in section 3.

	+12V	+5V	+3.3V	-12V	+5Vsb	Unit
Total DC Output Regulation	±5.0%	±5.0%	±5.0%	±5.0%	±5.0%	Volt

Table 2. DC Output Voltage Regulations

2.2.2. DC Output Load Distributions

The Table 3. defines the power supply typical output load distribution.

Output Rail	Output Voltage	Minimum Current (A)	850W Max (A)	750W Max (A)
V1	+12V	0.0	70.8	62.5
V2	+5V	0.0	25.0	25.0
V3	+3.3V	0.0	25.0	25.0
V4	-12V	0.0	0.3	0.3
V5	+5Vsb	0.0	3.0	3.0
Total Continuous Power			850W	750W
Max. combined O/P of V1			849.6W	750W
Max. combined O/P of V2 & V3			150W	150W

Table 3. DC Output Load Distribution (1000W/850W/750W)

Note: -12V at 0.3A maximum requires +12V rail to be loaded at 2A minimum.
Note: +12V at full load requires -12V rail to be loaded at 0.2A minimum.

2.2.3. DC Output Efficiency & Eup* requirements & Low Load Efficiency

DC Output Efficiency

The power supply efficiency is 80% minimum measured at 10%, efficiency is 87.0% minimum measured at 20%, efficiency is 90.0% minimum measured at 50%, efficiency is 87.0% minimum measured at 100% which is 115Vrms conditions. shown in Table 4.

	LOAD	+12V	+5V	+3.3V	-12V	+5VSB	SPEC(115/230Vac)
850W	10%	6.22A	1.06A	1.06A	0.03A	0.26A	>80%
	20%	11.82A	3.02A	3.02A	0.05A	0.50A	>87%
	50%	29.55A	7.54A	7.54A	0.13A	1.25A	>90%
	100%	59.11A	15.08A	15.08A	0.25A	2.5A	>87%
750W	10%	5.10A	1.48A	1.48A	0.02A	0.24A	>80%
	20%	10.21A	2.95A	2.95A	0.05A	0.49A	>87%
	50%	25.51A	7.38A	7.38A	0.120A	1.22A	>90%
	100%	51.03A	14.76A	14.76A	0.24A	2.45A	>87%

Table 4. The power supply typical output load distribution

Eup* requirements

In order to meet the 2010 and 2013 ErP Lot 6 requirements, AMS&2014 ErP Lot 3 requirements, and if any Computers use an Alternative Sleep Mode (ASM) then the 5V standby efficiency should be met as shown in Table 5 which is measured with the main outputs off (PS_ON# high state).

5VSB Load Target	5VSB Actual Load	Efficiency Target (both 115V and 230V input)	Remark
45mA		≥45%	ErP* Lot 6 2013
90mA		≥45%	ErP* Lot 6 2010
0.55A		≥75%	ASM and ErP* Lot 3 2014
1.00A		≥75%	Recommend
1.50A		≥75%	ASM and ErP* Lot 3 2014
Max / Label	3.0A /Label	≥75%	Recommend

Table 5. The power supply typical output load distribution

Low Load Efficiency

The lowest DC load for computers at this Idle Mode is determined to be 10 Watts for mainstream computers, shown in Table 6. Computers with PSU larger than 500 Watts are also expected to have more components and therefore the Idle Mode will be at a higher DC Load. The PSU above 500 Watts will use the Low Load Efficiency set at the 2% level. Low Load Efficiency levels will be phased in over time.

10W / 2% Low Load	60%

Table 6. Low Load Efficiency

2.2.4. DC Output Ripple & Noise

The output ripple & noise specifications listed in Table 7. will meet throughout the load ranges as specified in section 2.2.2 and the nominal line input voltage conditions as specified in section 2.1. Ripple & noise is defined as periodic of random signals over a frequency band of 10Hz to 20MHz. Measurements should be made with an oscilloscope with 20MHz bandwidth. adding a 10uF electrolytic capacitor and a 0.1uF ceramic capacitor across output terminal during ripple & noise measurement.

	+12V	+5V	+3.3V	-12V	+5Vsb	Unit
Max Ripple & Noise	80	50	50	60	50	mV P-P

Table 7. DC Output Ripple & Noise

2.2.5. DC Output Transient Response

The output voltages will remain within the regulation limits specified in Table 2. The load-changing repetition rate is 50Hz to 10KHz, and the transient load slew rate 1.0A/us. The maximum step load size, and output capacitive loading are specified as followings in Table 8.

	+12V	+5V	+3.3V	-12V	+5Vsb
Voltage limits.	±5%	±5%	±5%	±10%	±5%
Load Change Low Load	2.0%~72%	0.0%~30%	0.0%~30%	0A ~ 0.1A	0A ~ 0.5A
Load Change High Load	30%~100%	70%~100%	70%~100%	0.2A ~ 0.3A	2.0A ~ 2.5A
Capacitive Load	10000uF	10000uF	10000uF	470uF	3300uF

Table 8. DC Output Transient Response

2.2.6. DC Output Voltage Hold-up Time

The power supply will maintain outputs in regulation per section 2.2.1 despite a loss of input power at the nominal range of AC input and at 80% of maximum continuous output load as applicable for a minimum of 17 msec.

2.2.7 Load Transient Response (Step Load):

+12V Step load changes

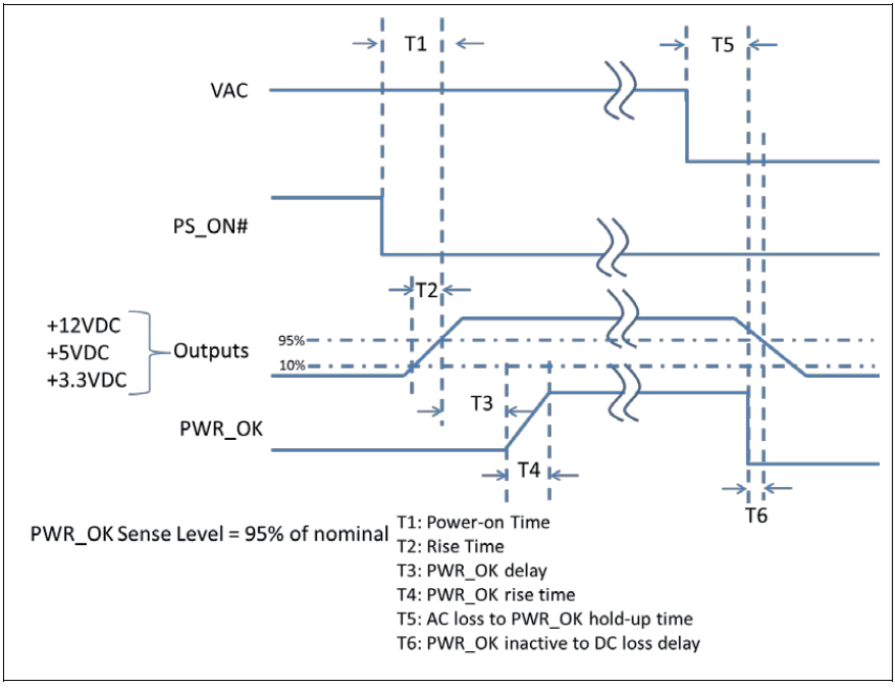
- ○ Up to 85%@min. load condition
- ○ PCI-E CEM5 100% to 200% for power excursion

Power Excursion % of PSU	Time for Power	Time Constant	Duty Cycle
100%	Infinite	---	---
120%	100ms	100ms	25%
160%	10ms	30ms	12.5%
180%	1ms	4ms	8%
200%	100us	900us	5%

+3.3V and +5V: 35% of full load

- Other loads remain constant within the rating.
- Load waveform
  - Square wave.
  - Slope of rise and fall at 5.0A/μS.
  - Frequency from 50Hz to 10KHz.
- DC output voltage will stay within regulation of +5/- 8% during the step load changes(+12V Capacitive Load 6600uF)

2.3. Timing / Housekeeping / control



Parameter	Description	Value		
		Required	Recommended for Non-Alternative Sleep Mode1	Recommended for Alternative Sleep Mode
T0	AC power on time	<2s		
T1	Power-on time	< 150ms	< 200ms	< 150ms
T2	Rise time	0.2 – 20 ms		
T3	PWR_OK delay	100 – 150 ms	100 – 250 ms	100 – 150 ms
T4	PWR_OK rise time	≤ 10 ms		
T5+T6	Hold up time	≥ 17 ms		80% Load
T6	PWR_OK inactive to DC loss delay	≥ 1 ms		

2.3.1. PWR_OK (Power Good Signal)

PWR_OK is a “power good” signal. It will be asserted high by the power supply to indicate that the +5V output is above the under voltage threshold listed in Table 2. of Section 2.2. PWR_OK will be de-asserted to a low state when +5V output voltage falls below under voltage threshold, or when AC power has been removed for a time sufficiently such that power supply operation cannot work normally. The electrical and timing characteristics of the PWR_OK signal are given in Table 9. and in figure 1.

Signal type	+5V TTL compatible
Logic level low	< 0.4 V while sinking 4 mA
Logic level high	Between 2.4 V and 5 V output while sourcing 200 μA
High-state output impedance	1 kΩ from output to common
Max Ripple/Noise	400 mV p-p

Table 9. PWR_OK Signal Characteristics

2.3.2. PS_ON (DC Soft Start)

PS_ON# is an active-low, TTL-compatible signal that allows a motherboard to remotely control the power supply in conjunction with features such as soft on/off, Wake on LAN*, or wake-on-modem. When PS_ON# is pulled to TTL low, the power supply should turn on the four main DC output rails: +12 VDC, +5 VDC, +3.3 VDC, and -12 VDC. When PS_ON# is pulled to TTL high or open-circuited, the DC output rails should not deliver current and should be held at zero potential with respect to ground. PS_ON# has no effect on the +5VSB output, which is always enabled whenever the AC power is present. Table 10 lists PS_ON signal characteristics.

	Min	Max
V_{IL}, Input Low Voltage	0.0V	0.8V
I_{IL}, Input Low Current (Vin = 0.4V)		-1.6mA
V_{IH}, Input high Voltage (Iin = -200uA)	2.0V	
V_{IH}, open circuit, Iin =0		-5.25V
Ripple/Noise		400 mV p-p

Table 10. PS_ON Signal Characteristics

2.3.3. +5Vsb (Standby Voltage Output)

+5Vsb is a standby voltage output that is active whenever the AC power is present. It provides a power source for circuits that must remain operational when the four main DC output rails are in a disabled state. Example uses include soft power control, Wake on LAN, wake on modem, intrusion detection, or suspend state activities. There is over current protection on the +5Vsb output to ensure the power supply will not be damaged if external circuits draw more current than the supply can provide.

2.3.4. Power-on Time

The power-on time is defined as the time from when PS_ON is pulled low to when the12V1, +5V, and +3.3V output are within the regulation ranges specified in Section 2.2.1. The power-on time will be less than 150ms (T1 <150 ms). +5Vsb has a power on time of two second max. after the valid AC Voltages applied.

2.3.5. Rise Time

The output voltage rise from ≤ 10% of nominal to within the regulation ranges specified in section 2.2.1 within 0.1 ms to 20 ms (0.1 ms ≤ T2 ≤ 20 ms)

2.3.6. Power Sequencing

The +12V1 and +5V output levels are equal to or greater than the +3.3V output at all times during power-up and normal operation. The time between the +12V1 or +5V output reaching its minimum in-regulation level and +3.3V reaching its minimum in-regulation level is ≤ 20 msec.

2.3.7. Overshoot at Turn-on / Turn-off

The output voltage overshoot upon the application or removal of the input voltage, or the assertion / de-assertion of PS_ON will be less than 10% above the nominal voltage.

2.3.8. Reset after Shutdown

If the power supply latches into a shutdown state because of a fault condition on its outputs, the power supply can return to normal operation only after the fault condition has been removed and the PS_ON has been cycled OFF/ON with a minimum OFF time of 1 second.

2.3.9. +5Vsb at AC Power-down

After AC power is removed, the +5Vsb standby voltage output will remain at its steady state value for the minimum hold-up time specified in Section 2.2.6 until the output begins to decrease in voltage. The decrease can be monotonic in nature, dropping to 0.0V. There are no other perturbations of this voltage at or following removal of AC power.

2.4. Output Protection

2.4.1. Over Voltage Protection

The power supply can provide latch-mode over voltage protection as defined in Table 11.

Output	Min.	Nom.	Max.	Unit
+12VDC	13.3	14.6	15.6	Volts
+5VDC	5.5	6.25	7.0	Volts
+3.3VDC	3.7	4.1	4.7	Volts

Table 11. Over Voltage Protection

2.4.2. Under Voltage Protection

The power supply can provide latch-mode Under voltage protection as defined in Table 12.

Output	Min.	Nom.	Max.	Unit
+12VDC	8.80	9.30	9.80	Volts
+5VDC	4.10	4.30	4.47	Volts
+3.3VDC	2.5	2.69	2.83	Volts

Table 12. Under Voltage Protection

2.4.3. Over Current Protection

The power supply can provide Over Current Protection as defined in Table 13.

Output	Min.	Max.
+12V1DC	115%	150%
+5VDC	30A	50A
+3.3VDC	35A	55A

2.4.4. Short-circuit Protection

The power supply will shut down and latch off for shorting the +12V, +5V, +3.3V, and -12V rails to return or any other rails. Shorts between main output rails and +5Vsb will not cause any damage to power supply. +5Vsb can be capable of being shorted indefinitely, but when the short is removed, the power supply will recover automatically or by cycling PS_ON. The power supply can be capable of withstanding a continuous short circuit to the output without damage or overstress to the unit under the input conditions specified in section 2.1.

2.4.5. Over Power Protection

Fold back at 110%~150% over peak load

2.4.6. OVER TEMPERATURE PROTECTION

>55 degree Celsius ambient at full load <70 degree Celsius ambient at 100Vac full load. Latch off protection with AC line or PWR_ON reset.

2.4.7. No-load Operation

No damage or hazardous condition will occur with all the DC output connectors disconnected from the load. The power supply may latch into the shutdown state.

2.4.8. Isolation (High Voltage Withstand)

1800Vac for 1 minute

3. Environmental

The following subsections define recommended environmental specifications and test parameters. Based on the typical conditions to which an ATX12V power supply may be subjected during operation or shipment.

3.1. Temperature

Operating	0°C to +50°C
Non-operating	-20°C to +70°C

3.2. Humidity

Operating	20% to 90% relative humidity (non-condensing)
Non-operating	5% to 95% relative humidity (non-condensing)

3.3. Altitude

Operating	0 to 10,000 feet
Storage	0 to 50,000 feet

4. Electromagnetic Compatibility

The following subsections outline applicable product regulatory specifications for this power supply.

4.1. Emissions (Meet)

The power supply can comply with FCC Part 15 and EN55032: 2015 meeting Class B for both conducted and radiated emissions with a 3 dB margin.

4.2. Immunity (Meet)

The power supply can comply with EN 55035: 2017.

4.3. CE Testing (Meet)

The following standards are applied during the CE testing

EN 55032: 2015	Class B with 3dB margin minimum
EN 61000-3-2: 2014	Harmonic Current Measurement
EN 61000-3-3: 2013	Voltage Fluctuation and Flick Measurement
EN 55035: 2017, including	
IEC 61000-4-2: 2009	ESD – air discharge 8kV / ESD contact discharge 4kV
IEC 61000-4-3: 2010	Radiated, Radio Frequency Electromagnetic Field mmunity Test
IEC 61000-4-4: 2012	Electrical Fast Transient/Burst Immunity Test
IEC 61000-4-5: 2014	Surge Immunity Test – 2kV L/N to PE and 1kV L to N
IEC 61000-4-6: 2014	Immunity to Conducted Disturbances Induced by RF Fields
IEC 61000-4-8: 2010	Power Frequency Magnetic Field Immunity Test
IEC 61000-4-11: 2004	Voltage Dips and Short Interruptions Immunity Test

5. Reliability

5.1. Component De-rating

The derating process promotes quality and high reliability. All electronic components are designed with conservative derating for use in commercial and industrial environments.

5.2. Mean Time between Failures (MTBF)

100K hours minimum at full load 25°C

6. Safety (Meet)

6.1. Safety

cTUVus	UL62368-1
TUV	EN 62368-1
CB	IEC 62368-1

6.2. RoHS & REACH Compliance

The power supply meets the requirements of RoHS & REACH Compliance specified as followings:

● European Directive for Waste of electrical and electronic equipment (WEEE) 2012/19/EU
● European Directive for Restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS) 2011/65/EU
● ACPEIP, Administration on the Control of Pollution caused by Electronic Information Products (China RoHS), e.g. SJ/T 11363-2006 Requirements for Concentration Limits for Certain Hazardous Substances in EIP, SJ/T 11364-2006 Marking for Control of Pollution Caused by EIP
● Plastic and rubber parts are within the limits for 16 PAH and Benzopyrene polycyclic aromatic hydrocarbons
- PAH (Polycyclic Aromatic Hydrocarbons):
  - 200mg/kg for components touched less than 30 seconds
  - 10mg/kg for components touched longer than 30 seconds
- Benzopyrene are within the limits of:
  - 20mg/kg for components touched less than 30 seconds
  - 1mg/kg for components touched longer than 30 seconds

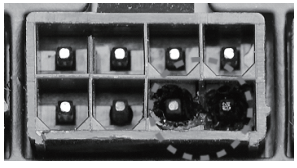
● Phthalate concentration is below 1000mg/kg for:
- Diisononyl phthalate
- Bis(2-ethylhexyl)phthalate
- Di-n-octyl phthalate
- Diisodecyl phthalate
- Butyl benzyl phthalate
- Bis(n-butyl)phthalate

● Polychlorinated biphenyl (PCB) concentration limits are less than two (2) parts per million (ppm).
Regulation (EC) No 1907/2006 ... concerning the Registration, Evaluation, Authorization and Restriction of Chemicals (REACH): No substance of Very High Concern of the “Candidate List” exceeds more than 0,1 % of the global weight of the delivered item (without packaging of the item)

7. Mechanical

Dimension W*L*H=100x190x70mm

8. Power Supply Connector Overuse Definition



DE Definition einer Überlastung des Netzanschlusses

FR Définition de l'utilisation excessive du connecteur d'alimentation électrique

ES Definición de uso excesivo del conector de la Fuente de alimentación

IT Definizione di uso eccessivo del connettore di alimentazione

RU Определение чрезмерной нагрузки на коннектор блока питания

TH

KR 전원 공급 커넥터 과용 정의

JP 電力供給コネクタの使用限度超過に関する説明

CN 电源供应器接头过度使用定义

TW 電源供應器接頭過度使用定義

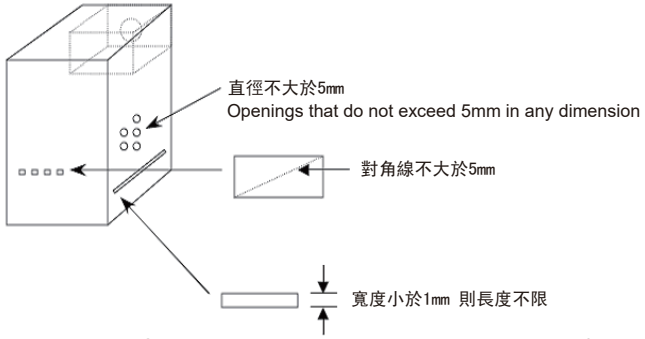
1. 為了保護使用者及防火的目的，安裝此交換式電源供應器時，必須安裝於符合下列各項要求的外殼中，並且安裝妥善後，才可接上電源。

1-1. 外殼材質須為防火外殼。外壳材质须为防火外壳。

1-2. 外殼的上方及側邊之圓形開孔，最大內徑不可大於5mm。

1-3. 外殼的上方及側邊之長條型開孔，對角線距離不可大於5mm；若寬度小於1mm，則長度不受限制。

1-4. 外殼底部不可有開孔。外壳底部不可有开孔。



2. 本產品輸出含有危險能量，為避免操作時發生危險，須於裝入系統機殼並將所有設備安裝妥當後才可開啟電源。

3. 本產品之電源輸出非屬電力限制型電源，請連接使用具防火外殼之周邊，以避免火災危險發生。

BSMI ROHS 資訊
http://www.silverstonetek.com/downloads/PSU/RSD.pdf

开关电源供应器　有毒有害物质/元素及其化学含量表

部件名称	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
外壳	○	○	○	○	○	○
接头	○	○	○	○	○	○
风扇	○	○	○	○	○	○
电子卡	○	○	○	○	○	○
线材	○	○	○	○	○	○
螺丝	○	○	○	○	○	○
包材	○	○	○	○	○	○

本表格依据SJ/T 11364的规定编制

○：表示该有毒有害物质在该部件所有均质材料中的含量均在GB/T 26572 规定的限量要求以下。

×：表示该有毒有害物质至少在该部件的某一均质材料中的含量超出GB/T 26572 规定的限量要求。

10 产品合格证

This device complies with Part 15 of the FCC Rules.

Operation is subject to the following two conditions:

(1) this device may not cause harmful interference, and
(2) this device must accept any interference received, including interference that may cause undesired operation.

SST-2U750R-GF / SST-2U850R-GF

Model (safety certification):SST-2U0750FCGD-A
SST-2U0850FCGD-A

The equipment a Class | Switching Power Supply intended to use for information technology equipment or Audio and Video equipment.

※付属の電源コードは当該製品専用です。他の機器に使用しないでください。

Please refer to SilverStone website for latest specifications updates.

SilverStone Technology Co., Ltd.

www.silverstonetek.com

support@silverstonetek.com
NO.G11255890